7	Complete If Known		
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Sheet 1 of 1	Attorney Docket Number	60809-5083-US (formerly 017424-0316628)	

			U.S. PATEN	T DOCUMENTS					
Examiner Initials	Cite No.	Document Number Number - Kind Code ¹	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Class	Subclass		Date if priate	
		<u> </u>	FOREIGN PAT	ENT DOCUMENTS	1	<u> </u>	<u> </u>		
		Foreign Patent			T	<u> </u>	Translation		
Examiner Initials	Cite	Document Country Code ² -	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Class	Subclass	Translation		
	No.	Number ³ - Kind Code ⁴ (if known)					Yes	No	
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				ITERATURE DOCUMENT		•		_	
Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published							
JC		International Search Report for International Application No. PCT/EP03/15036, filing date							
		December 24, 2003.							
JC		ABUSHAABAN, M., et al., "Modal Circuit Decomposition of Lossy Multiconductor							
		Transmission Lines," IEEE Transactions on Microwave Theory and Techniques, Vol. 44, No.							
		7, July 1996, pp. 1046-1056.							
JC.		EL-ZEIN, A., et al., "An Analytical Method for Finding the Maximum Crosstalk in Lossless-							
		Coupled Transmission Lines," Proceedings of the IEEE/ACM Int'l Conf. on Computer Aided							
JC	<u> </u>	Design, Santa Clara, California, Nov. 8-12, 1992, pp. 443-448. HUIJSING, J.H., "Operational Floating Amplifier," J. of IEEE Proceedings, Vol. 137, Pt. G.,							
		No. 2, April 1990.							
JC		LI, G., et al., "Line-Modes Decomposition of Three-Conductor Transmission Lines,"							
		Microwave Conf. 2000, Sydney, Australia, Dec. 3-6, 2000.							
NGUYEN, T.H., et al., "Propagation Over Multiple Parallel Transmission Lir							ia Mod	es,"	
JC		IBM Technical Disclosure Bulletin, IBM Corp., Vol. 32, No. 11, April 1990.							
JC		NICKEL, J.G., et al., "Frequency-Domain-Coupled Microstrip-Line Normal-Mode Parameter							
		Extraction From S-Parameters," IEEE Transactions on Electromagnetic Compatibility, Vol. 43,							
	ļ	No. 4, November 2001, pp. 495-503.							
JC		OTT, H.W., "Noise Reduction Techniques in Electronic Systems," Chapter 4, 2d Ed., John Wiley & Sons, 1988.							
				nission-Line Equation Un					
JC	İ	Assumption," IEEE Transactions on Electromagnetic Compatibility, Vol. 44, No. 3, August 2002, pp. 413-423.							

Examiner	/Jason Crawford/	Date	08/29/2006
Signature	, , , , , , , , , , , , , , , , , , , ,	Considered	00/23/2000

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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